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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Christopher Kalivoda

Joo-Hoon Lee

Group Art Unit: 2881

Application No.: 10/600,686

Filed: June 20, 2003

For: METHOD FOR FABRICATING PLANAR LIGHT WAVEGUIDE CIRCUITS WITH VERTICAL TAPER STRUCTURE

Commissioner for Patents Mail Stop Non-Fee Amendment Box 1450 Alexandria, Virginia 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated March 17, 2004, please amend the above-identified application as follows: